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FIG. 1
(PRIOR ART)

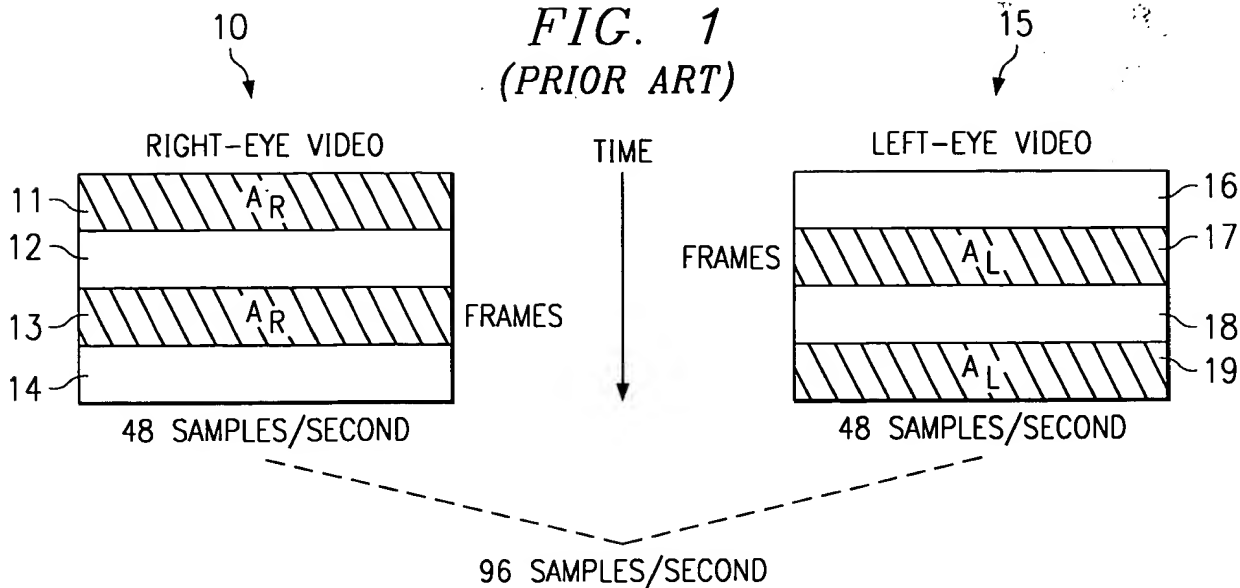


FIG. 2
(PRIOR ART)

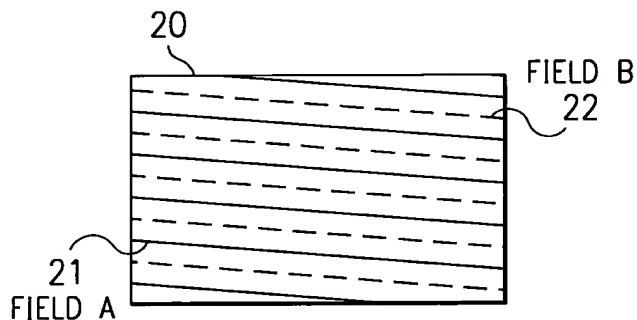
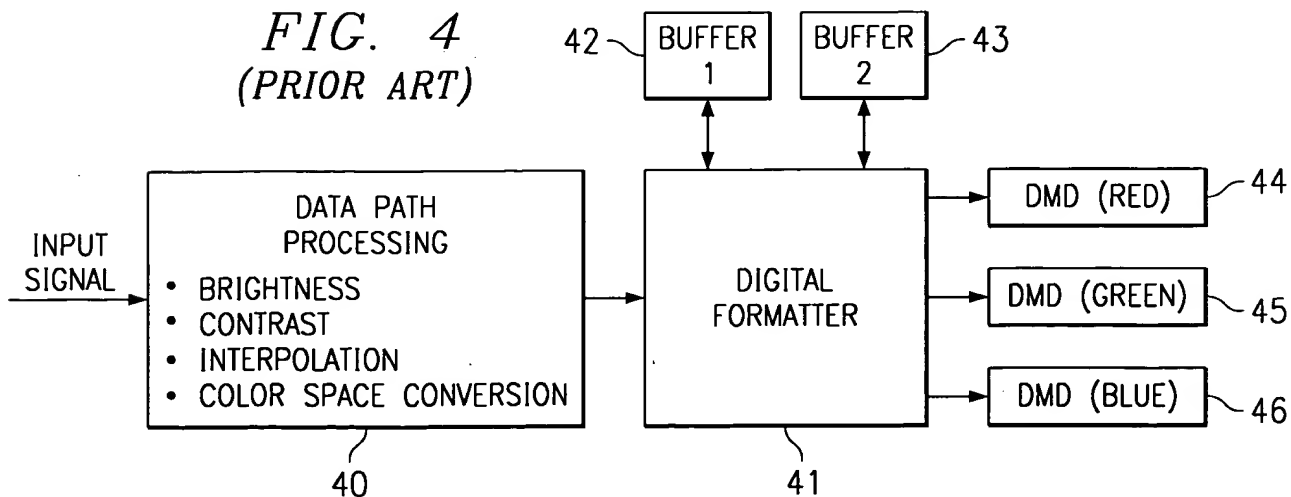
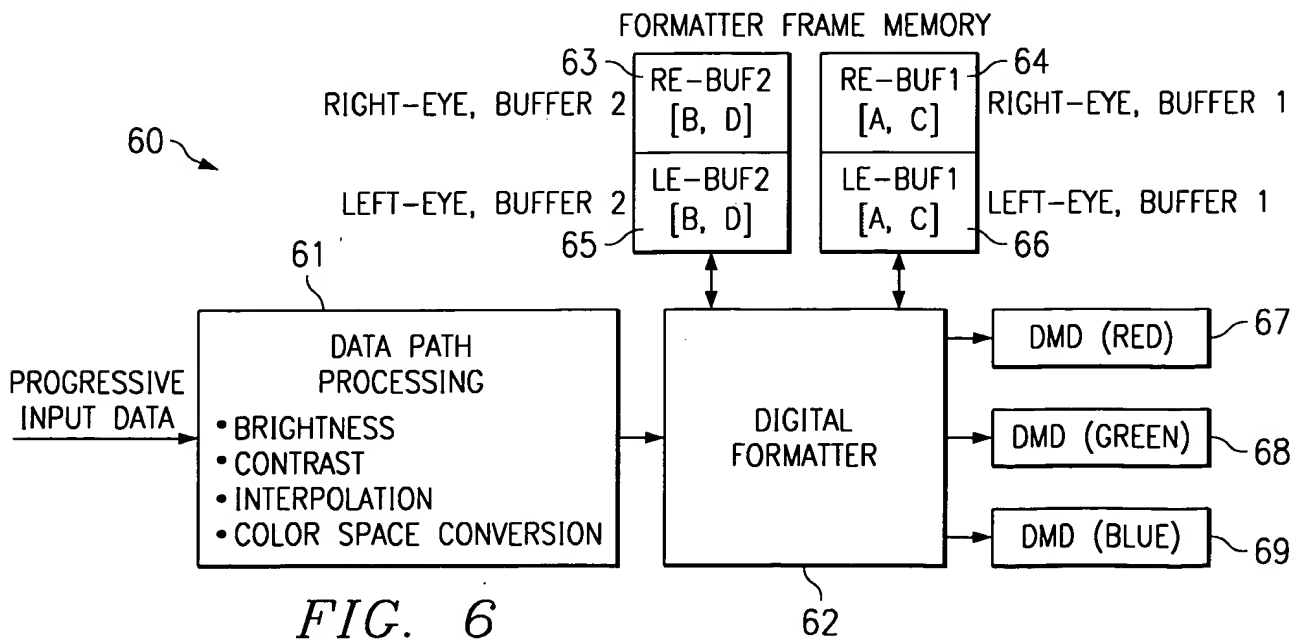
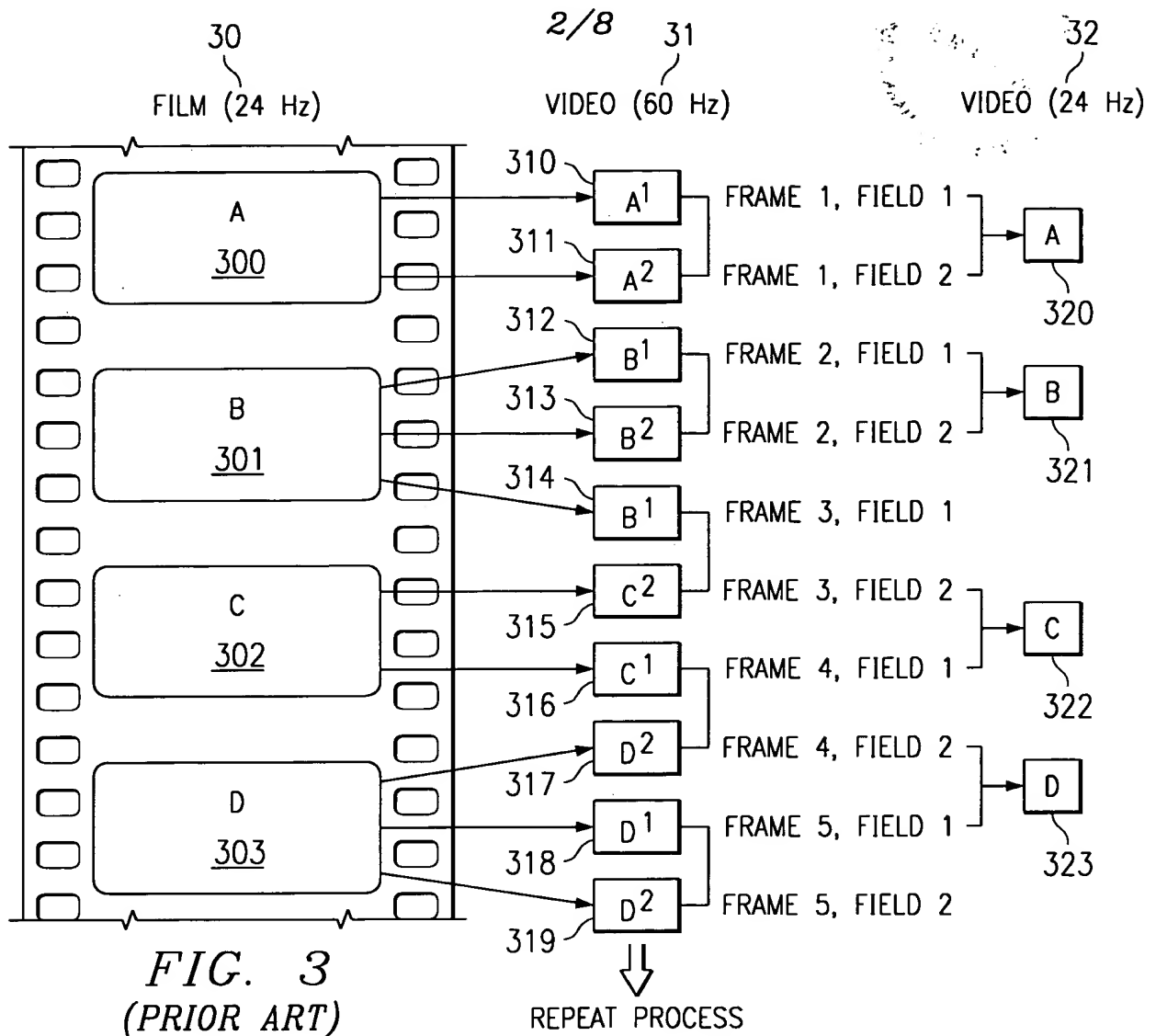


FIG. 4
(PRIOR ART)





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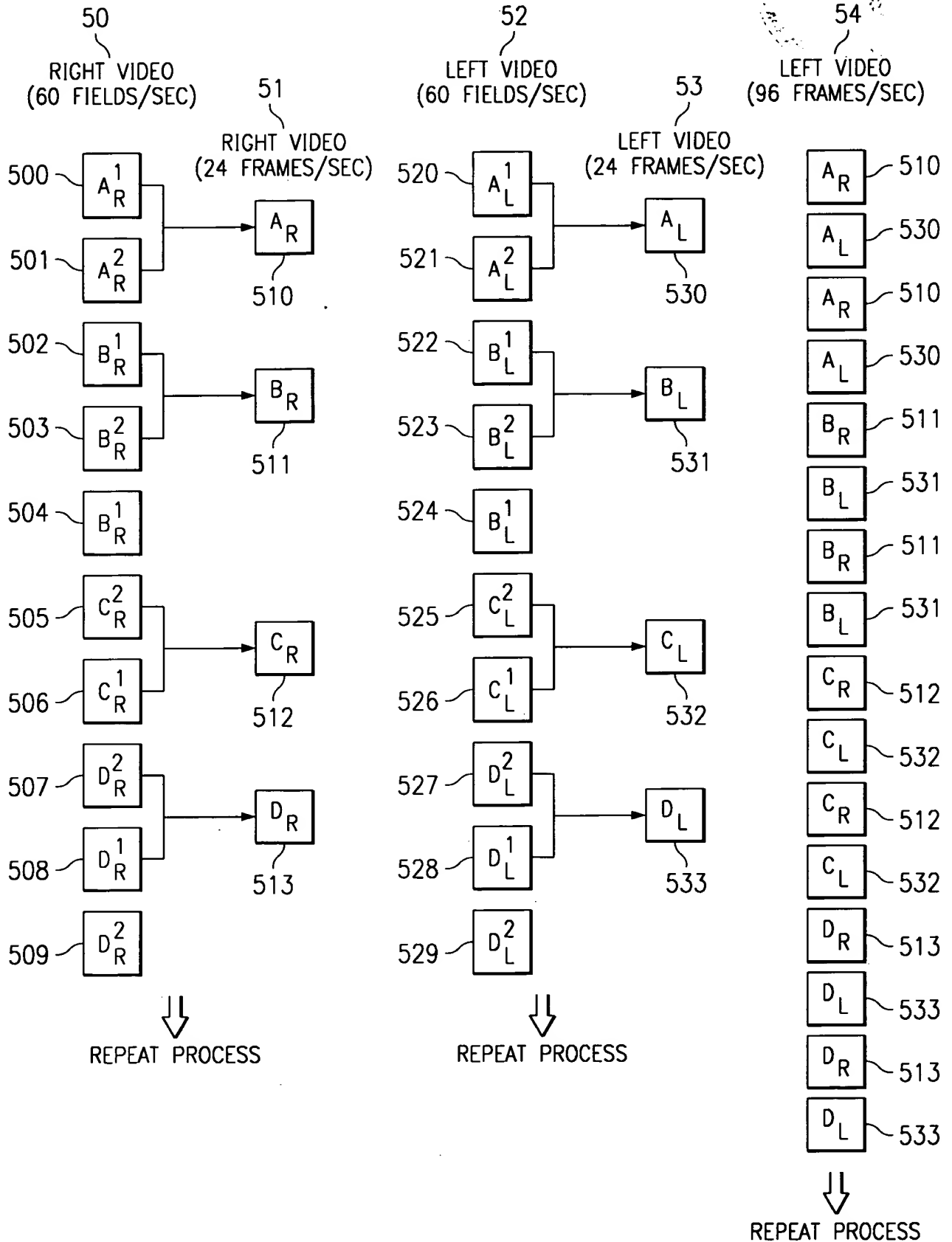


FIG. 5

FIG. 7

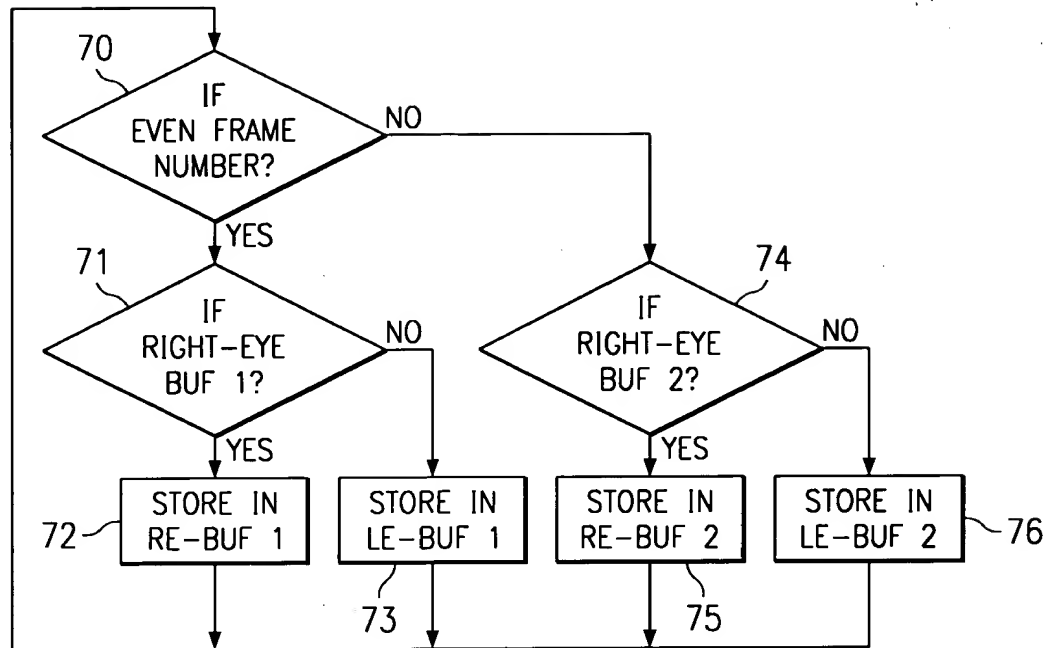
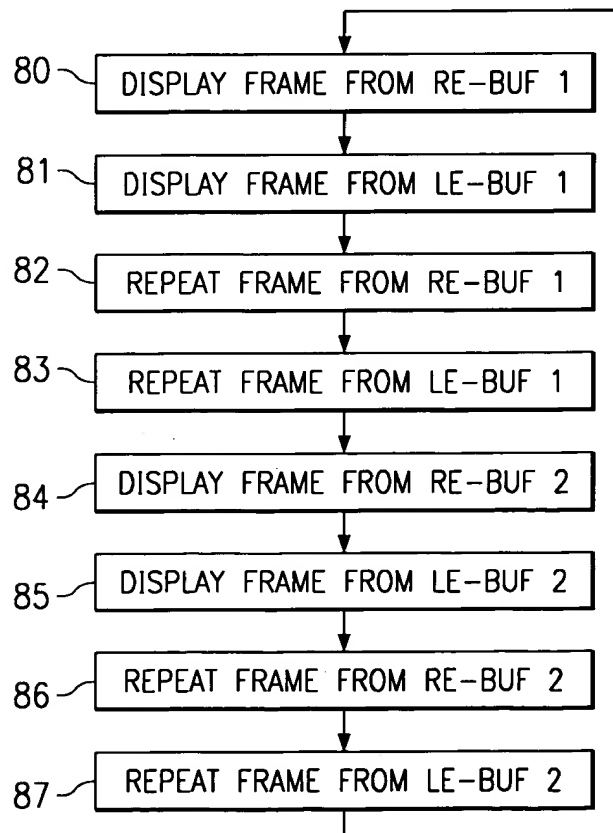


FIG. 8



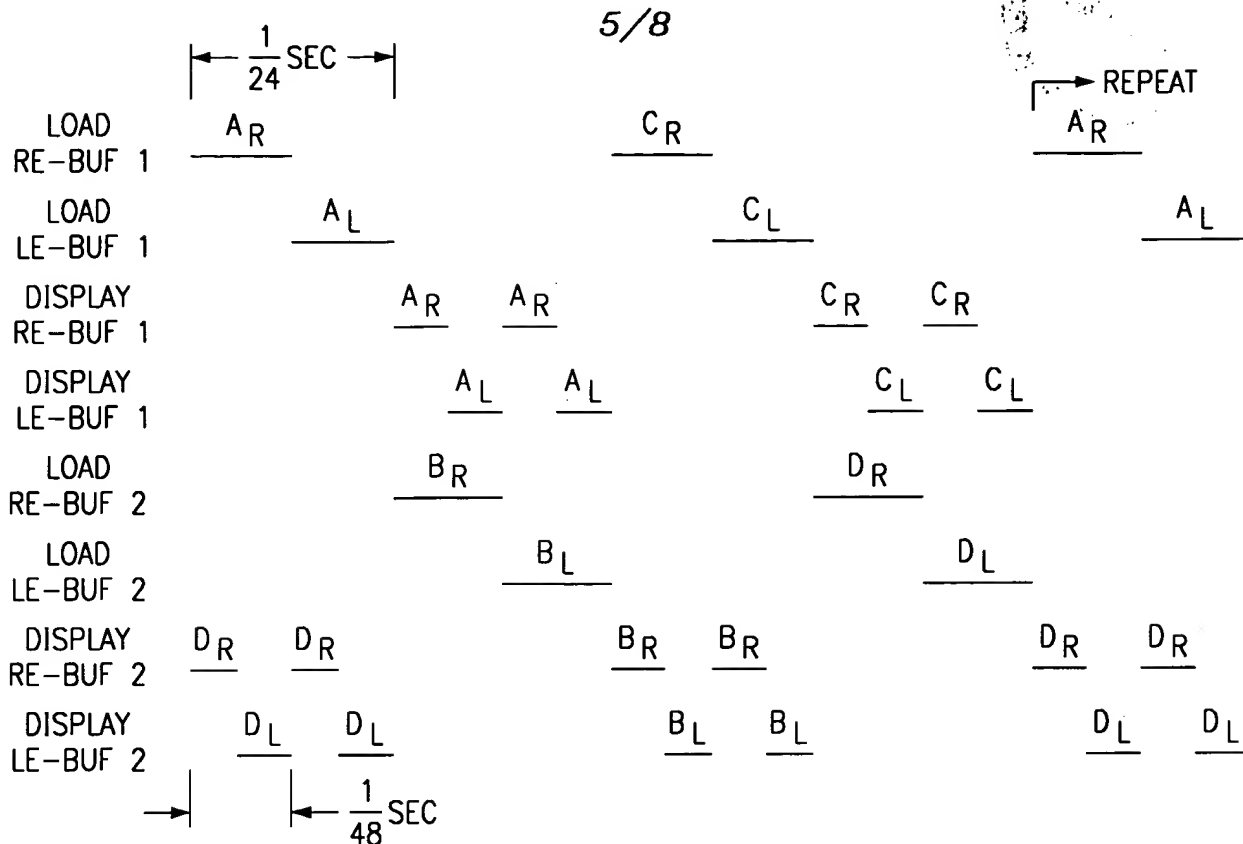


FIG. 9

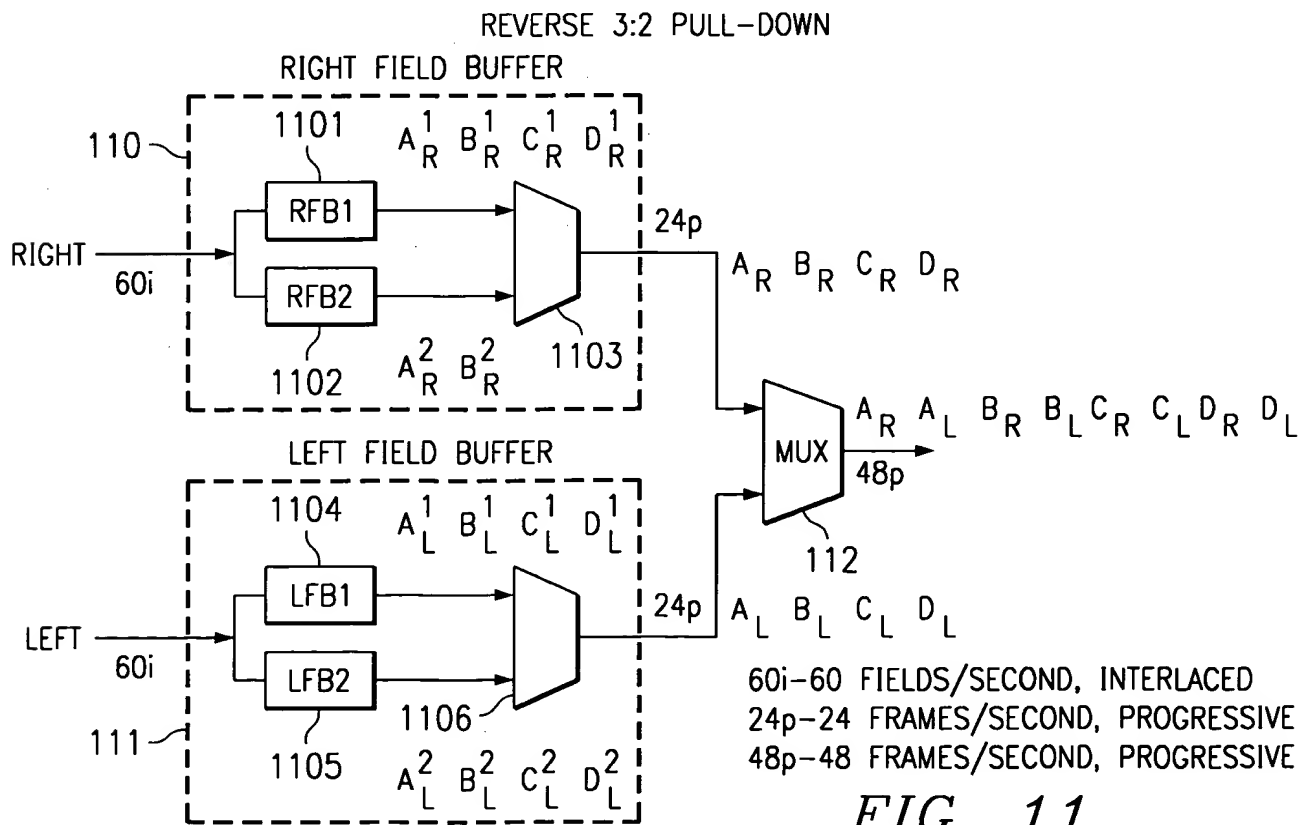


FIG. 11

TOP SECRET

FIG. 10

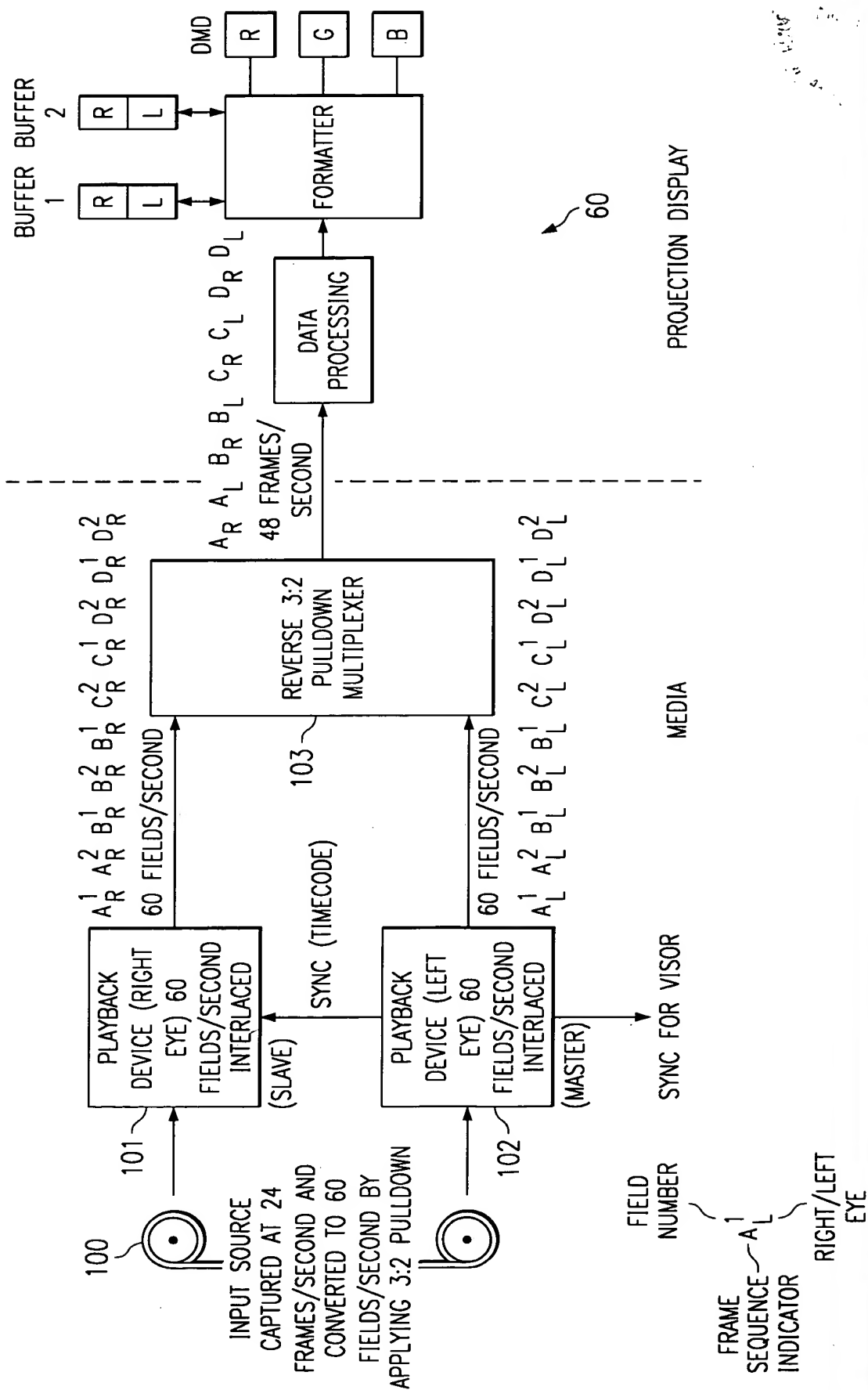


FIG. 10

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FIG. 12

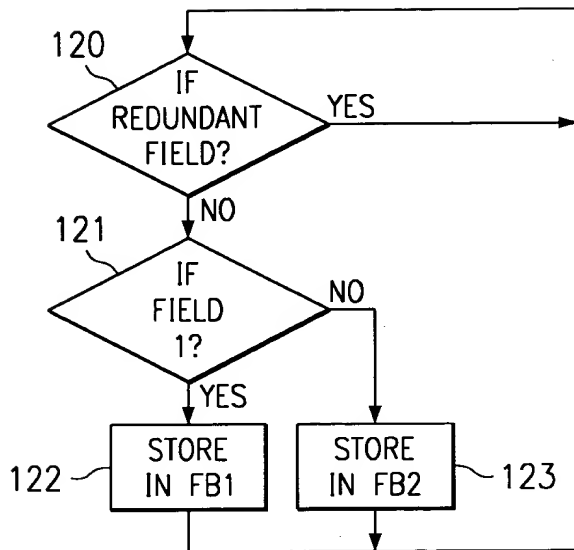


FIG. 13

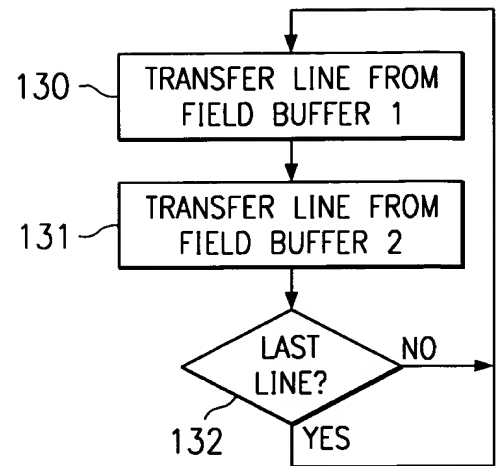
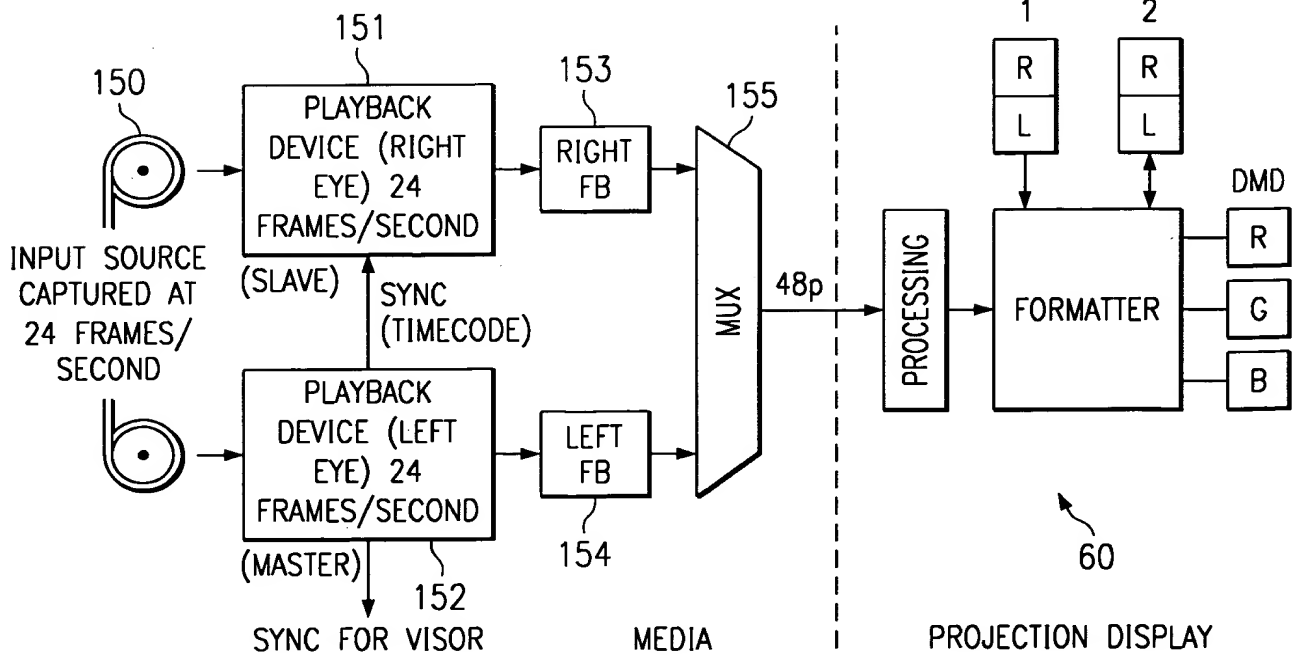
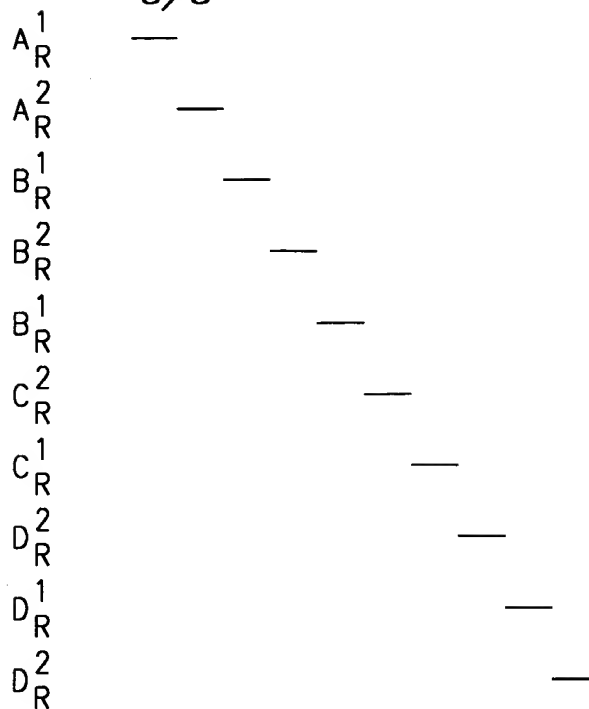


FIG. 15



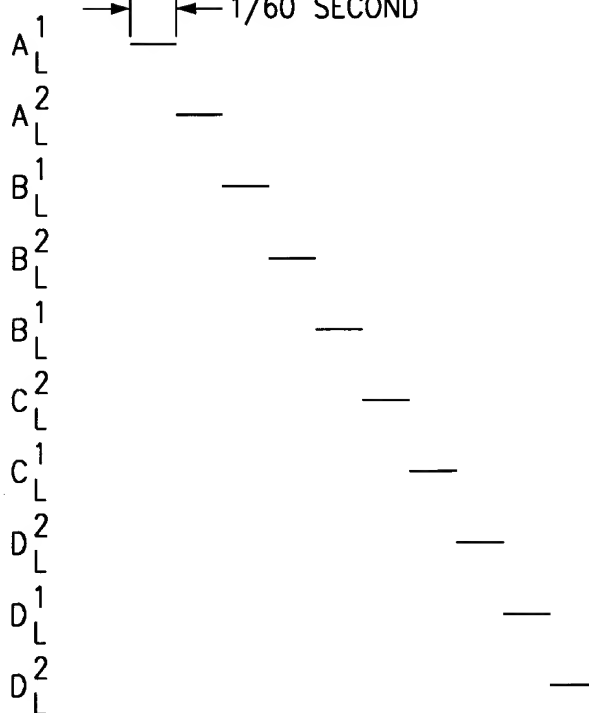
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INPUT TO RIGHT
FIELD BUFFER 110



1/60 SECOND

INPUT TO RIGHT
FIELD BUFFER 111



1/48 SECOND

INPUT TO
MUX 112

RFB
LFB

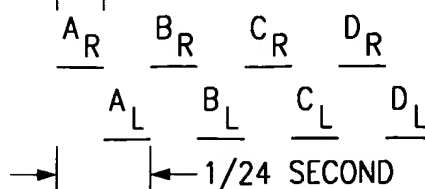


FIG. 14